

## REMARKS

One paragraph of the specification has been amended to correct the frequency of the clock that is coupled to the 68331 microcontroller. The 68331 microcontroller is made by Motorola Inc. as explained on page 67, lines 6 and 7 of the application as filed. Those skilled in the art will appreciate that the clock frequency for a Motorola 68331 microcontroller is 32.768 kHz, not 32768 kHz. In fact, the data sheet pertaining to the Motorola 68331 microcontroller, which is readily available on the Internet or from Motorola Inc., specifies that operation with a 32.768 kHz clock is standard. Also, in the last line of the amended paragraph, a space has been added between "15" and "pF."

Several amendments have been made to the drawings. These amendments are shown in the accompanying Replacement Sheets and also in the Annotated Sheets Showing Changes which are attached hereto as Appendix A. It is noted that the set of drawings mailed on December 27, 2001 along with the RESPONSE TO NOTICE TO FILE MISSING PARTS OF NONPROVISIONAL APPLICATION had some handwritten notes thereon pertaining to some of the drawing corrections being made by this Amendment. Due to the large number of drawing sheets in this case (i.e., 180 drawing sheets), neither Applicants' attorneys nor the Patent Office noticed this inadvertent oversight. Applicants' attorneys intended to make the corrections that were indicated on the drawings by the handwritten notes in response to the first Office Action not in the RESPONSE TO NOTICE TO FILE MISSING PARTS OF NONPROVISIONAL APPLICATION.

Figs. 56 and 58 have been amended to change the word "where" to "were" to correct the typographical error in each of these Figs. In Fig. 62E, 32768 kHz is replaced by 32.768 kHz to match the correction made to the specification. In Fig. 62G, a missing line segment to connect up to capacitor C49 has been added. In Fig. 62H, a line has been added atop CS10 on the pin 10 designation and QSO has been replaced by DSO on the pin 77 designation. In Fig. 62I, the pin 69 designation has been changed from PSC0/SS/PQS3 to PSC1/PQS4, the pin 68 designation has been changed from TXD/PQS7 to PSC0/SS/PQS3 (with a line above the SS), and the pin 75 designation has been changed from ADDR0 to TXD/PQS7. In Fig. 62R, a line has been added over VFD\_CE. In Fig. 63F, MEMA (with a line thereover) has been replaced by MEMR (with a line thereover). In Fig. 64M, a line segment connecting up the output of the lowermost amplifier (designated as U29) with the associated node has been added. In Fig. 66 J, "M SO" has been replaced with MISO.

The Examiner rejected claims 1-139 under 35 U.S.C. § 103(a) as being unpatentable over U.S. Pat. No. 6,421,571 to Spriggs et al. and U.S. Pat. Application Publication No. 2002/0020444 A1 to Dickerson, JR. et al. Submitted concurrently herewith is a Declaration Under 37 C.F.R. § 1.131 to antedate (i.e., swear behind) U.S. Pat. No. 6,421,571 and U.S. Pat. Application Publication No. 2002/0020444 A1, thereby overcoming the Examiner's rejection. Accordingly, claims 1-139 are in condition for allowance and such action is respectfully requested.

With regard to the Office Action mailed June 27, 2003, Applicants point out that the Examiner's statement that, "[a]t this point in the office action, the remaining independent claims 75, 100, 112, 120, 123, 124, 130, and 137 are obvious over the combination of Spriggs and Dickerson as set forth above," which appears on Page 5 of the Office Action, fails to make out a prima facie case of obviousness regarding these claims because independent claims 75, 100, 120, 123, 124, 130, and 137 have different limitations than the two independent claims (i.e., claims 1 and 27) to which the Examiner applied the cited art. The Examiner makes no attempt to explain how the cited references are being applied to the limitations that actually appear in claims 75, 100, 112, 120, 123, 124, 130, and 137. Furthermore, with regard to independent claim 27, the Examiner's explanation for making the rejection is that "gas monitoring is suggested in Dickerson." This statement does not make out a prima facie case of obviousness with regard to claim 27 because it does not even mention the actual claim limitations appearing in claim 27, let alone identifying how the cited references, in combination, teach or suggest all of the claim 27 limitations. In addition, the Examiner paraphrases many of the dependent claims in a manner inconsistent with the language that actually appears in these claims. It is respectfully requested that, in the event that the Examiner makes any claim rejections in any future Office Actions, a more thorough explanation of the basis for the rejection(s) be provided to assure that the prior art reference (or references when combined) teach or suggest all of the claim limitations which, among other things, is required in order to make out a prima facie case of obviousness.

If there are any questions or comments that would speed prosecution of this patent application, the Examiner is invited to call the undersigned at (317) 231-7341.

It is respectfully requested that, if necessary to effect a timely response, this paper be considered as a Petition for an Extension of Time sufficient to effect a timely response and that shortages in fees, if any, be charged, or any overpayment in fees credited, to the Account of Barnes & Thornburg, Deposit Account No. 10-0435 with reference to file 7175-68263.

Respectfully submitted,  
BARNES & THORNBURG

A handwritten signature in black ink, reading "Ronald S. Henderson". The signature is fluid and cursive, with a long horizontal flourish at the end.

Ronald S. Henderson  
Attorney Reg. No. 43669

Indianapolis, Indiana  
317-231-7341  
INDS02 RSH 605148v1



# AMENDED SHEET SHOWING CHANGES

[View Area](#)  
[Home](#)  
[Gas Readings](#)  
[Device Information](#)  
[Masters](#)  
[Alarms](#)  
[Event Log](#)

[Setup Area](#)  
[Setup Device](#)  
[Setup Network](#)  
[Set Clock](#)  
[Administrate Users](#)  
[Update Flash](#)  
[Log Out](#)

[Diagnostics](#)  
[Network Statistics](#)

[Help](#)

Set Clock

Area Communications Module

Current Time: 12-Jul-2001 15:47:02 ← 842

Year 844 → 2001 856  
Month 846 → July 856  
Date 848 → 12 856  
Hour 850 → 15 856  
Minute 852 → 47 856  
Second 854 → 2 856

858 →   ← 860

Fig. 55

[View Area](#)  
[Home](#)  
[Gas Readings](#)  
[Device Information](#)  
[Masters](#)  
[Alarms](#)  
[Event Log](#)

[Setup Area](#)  
[Setup Device](#)  
[Setup Network](#)  
[Set Clock](#)  
[Administrate Users](#)  
[Update Flash](#)  
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[Diagnostics](#)  
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[Help](#)

Area Comms - Change Result

Changes where accepted → were ← 864  
The clock is now set to 12-Jul-2001 15:47:02

Fig. 56



## AMENDED SHEET SHOWING CHANGES

<u>View Area</u> <u>Home</u> <u>Gas Readings</u> <u>Device Information</u> <u>Masters</u> <u>Alarms</u> <u>Event Log</u>  <u>Setup Area</u> <u>Setup Device</u> <u>Setup Network</u> <u>Set Clock</u> <u>Administrate Users</u> <u>Update Flash</u> <u>Log Out</u>  <u>Diagnostics</u> <u>Network Statistics</u>  <u>Help</u>	<h3>User Administration</h3> <p>Area Communications Module</p> <p>These entries are case sensitive</p> <table><thead><tr><th></th><th>User Name</th><th>Password</th></tr></thead><tbody><tr><td>User 1 Name</td><td><input type="text" value="new"/></td><td><input type="password"/></td></tr><tr><td>User 2 Name</td><td><input type="text"/></td><td><input type="password"/></td></tr><tr><td>User 3 Name</td><td><input type="text"/></td><td><input type="password"/></td></tr></tbody></table> <p><input type="button" value="Submit"/> <input type="button" value="Reset"/></p>		User Name	Password	User 1 Name	<input type="text" value="new"/>	<input type="password"/>	User 2 Name	<input type="text"/>	<input type="password"/>	User 3 Name	<input type="text"/>	<input type="password"/>
	User Name	Password											
User 1 Name	<input type="text" value="new"/>	<input type="password"/>											
User 2 Name	<input type="text"/>	<input type="password"/>											
User 3 Name	<input type="text"/>	<input type="password"/>											

868 points to User Name header  
870 points to Password header  
872 points to Submit button  
874 points to Reset button

Fig. 57

866

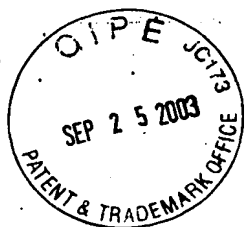
876

<u>View Area</u> <u>Home</u> <u>Gas Readings</u> <u>Device Information</u> <u>Masters</u> <u>Alarms</u> <u>Event Log</u>  <u>Setup Area</u> <u>Setup Device</u> <u>Setup Network</u> <u>Set Clock</u> <u>Administrate Users</u> <u>Update Flash</u> <u>Log Out</u>  <u>Diagnostics</u> <u>Network Statistics</u>  <u>Help</u>	<h3>Change User Info Result</h3> <p>Area Communications Module</p> <p>Changes to user name and password were accepted</p>
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876 points to Administrate Users link

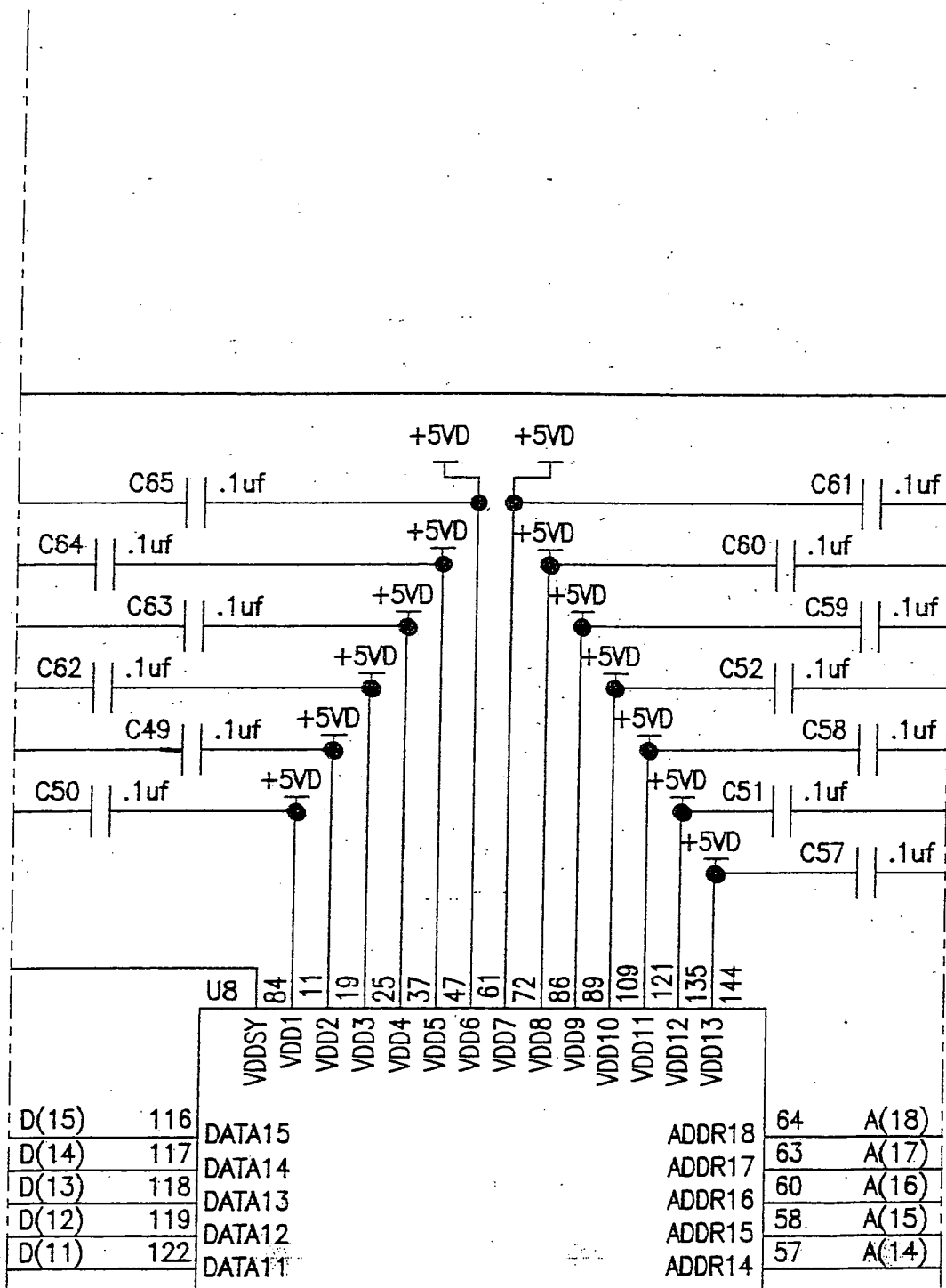
Fig. 58

The schematic diagram illustrates the MAX809 reset circuit. The MAX809 chip has three pins: VCC (pin 3), RESET (pin 2), and GND (pin 1). The VCC pin is connected to a +5VD supply through a resistor R142 (866 ohms) and a capacitor C53 (.1uf). The RESET pin is connected to a +5VD supply through a resistor R140 (332K) and a capacitor C66 (15pf). The GND pin is connected to DGND through a capacitor C67 (15pf). A crystal Y1 (32.768KHZ) is connected between the +5VD supply and the RESET pin. Resistor R135 (10M) is connected between the +5VD supply and the RESET pin. Resistor R103 (10K) is connected between the +5VD supply and the RESET pin. Resistor R113 (10K) is connected between the +5VD supply and the RESET pin. Resistor R155 (10K) is connected between the +5VD supply and the RESET pin. Resistor R117 (10K) is connected between the +5VD supply and the RESET pin.



# AMENDED SHEET SHOWING CHANGES

FIG. 62G





AMENDED SHEET SHOWING CHANGES

FIG. 62H

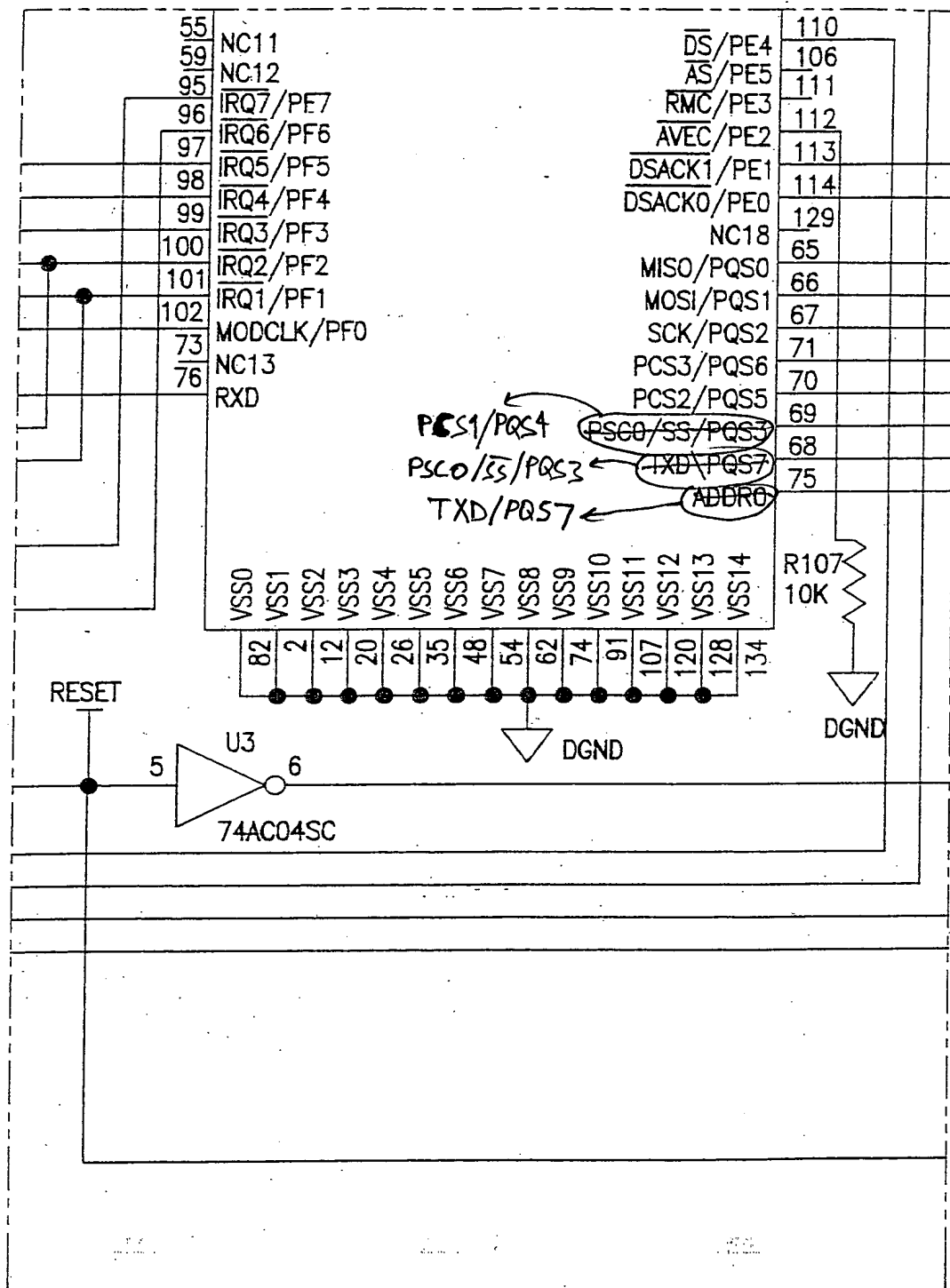
D(10)	124	DATA10	ADDR13	56	A(13)
D(9)	125	DATA9	ADDR11	52	A(12)
D(8)	127	DATA8	ADDR12	51	A(11)
D(7)	130	DATA7	ADDR10	50	A(10)
D(6)	131	DATA6	ADDR9	49	A(9)
D(5)	132	DATA5	ADDR8	46	A(8)
D(4)	133	DATA4	ADDR7	45	A(7)
D(3)	136	DATA3	ADDR6	44	A(6)
D(2)	137	DATA2	ADDR5	43	A(5)
D(1)	138	DATA1	ADDR4	42	A(4)
D(0)	139	DATA0	ADDR3	41	A(3)
	1	NC1	ADDR2	40	A(2)
	16	NC2	ADDR1	39	A(1)
	17	NC3	ADDR0	115	A(0)
	87	XFC	NC14	88	A(19)
	85	EXTAL	ADDR23/ <del>CS10</del>	10	
	83	XTAL	ADDR22/CS9/PC6	9	
	18	NC4	ADDR21/CS8/PC5	8	
	21	NC5	ADDR20/CS7/PC4	7	
	27	NC6	ADDR19/CS6/PC3	6	
	94	BERR	CS5/FC2/PC2	5	
	93	HALT	CS4/FC1/PC1	4	
	92	RESET	CS3/FC0/PC0	3	
	79	BKPT/DSCLK		108	
	80	TSC	NC15	143	
	34	NC7	BGACK/CS2	142	
	36	NC8	BG/CS1	141	
	23	IC4/OC5/OC1/PGP7	BH/CS0	140	
	24	OC4/OC1/PGP6	CSB00T	123	
	28	OC3/OC1/PGP5	NC16	103	
	29	OC2/OC1/PGP4	R/W	90	
	30	OC1/PGP3	CLKOUT	15	
	31	IC3/PGP2	PWMA	14	
	32	IC2/PGP1	PWMB	81	
	33	IC1/PGP0	FREEZE/OOUT	77	
	38	NC9	DSO ← IPIPE/ <del>CS0</del>	78	
	53	NC10	IFETCH/DSI	126	
	22	PAI	NC17	104	
	13	PCLK	SIZ1/PE7	105	
			SIZ0/PE6		





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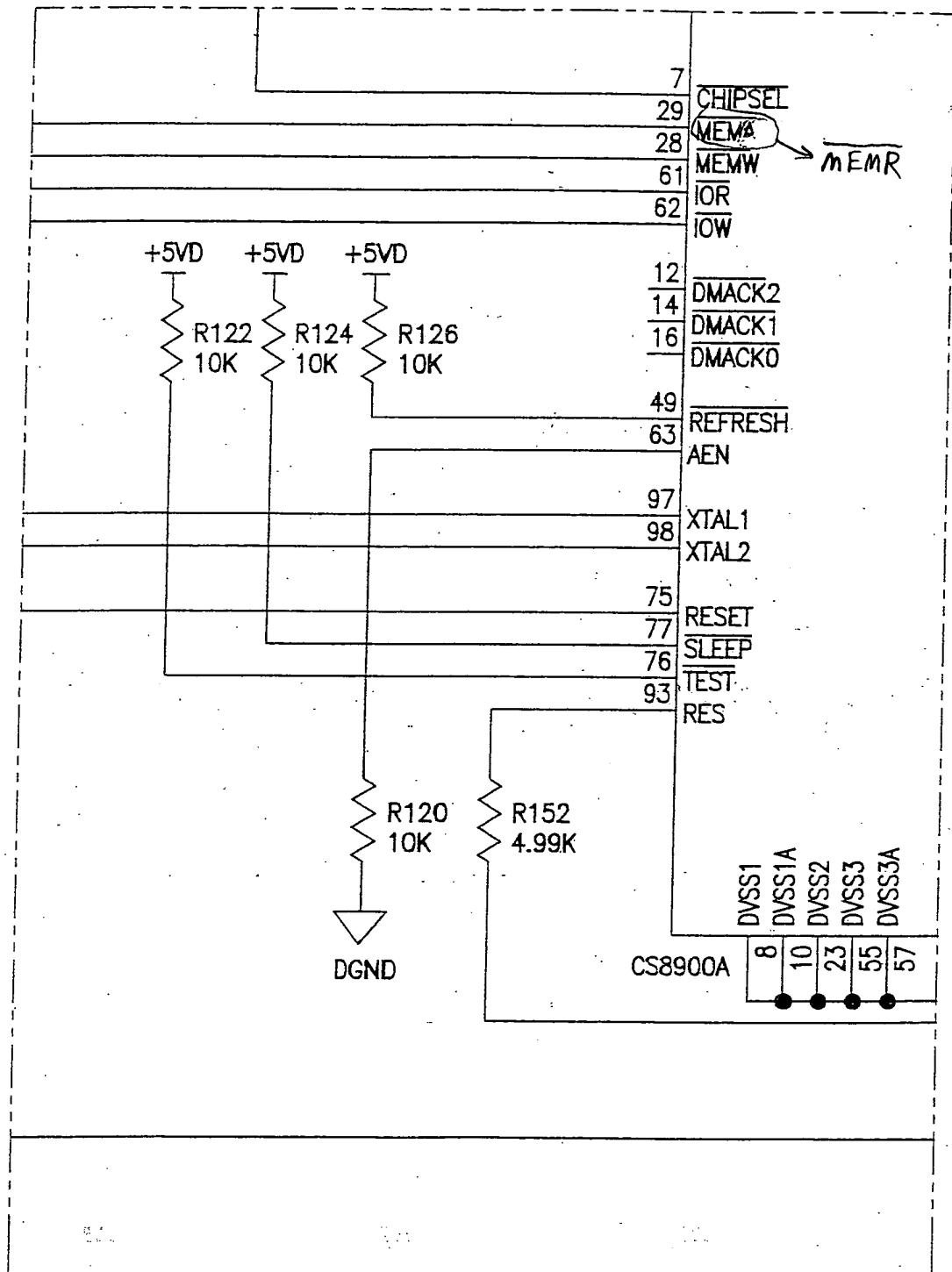
FIG. 621





AMENDED SHEET SHOWING CHANGES

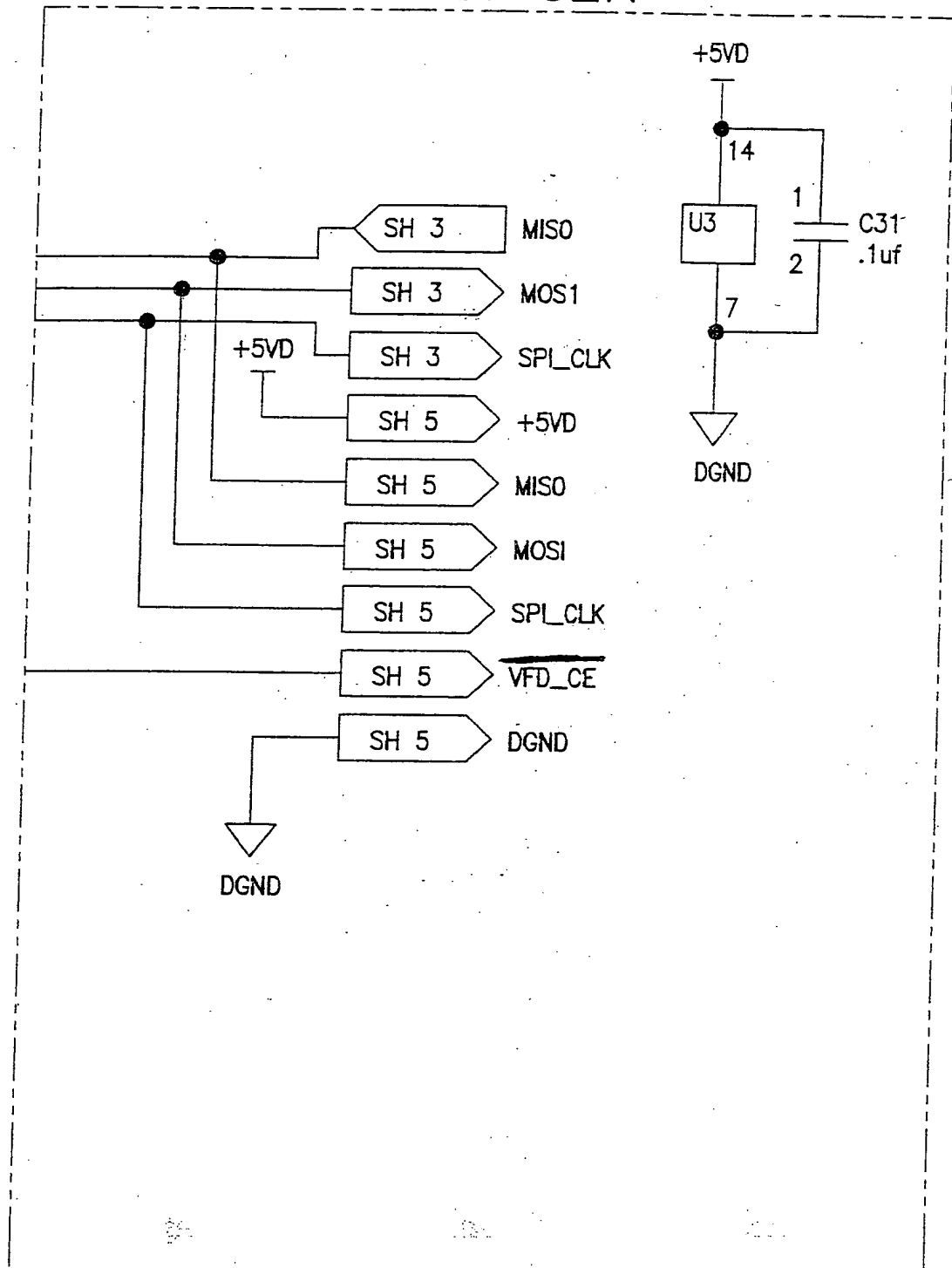
FIG. 63F





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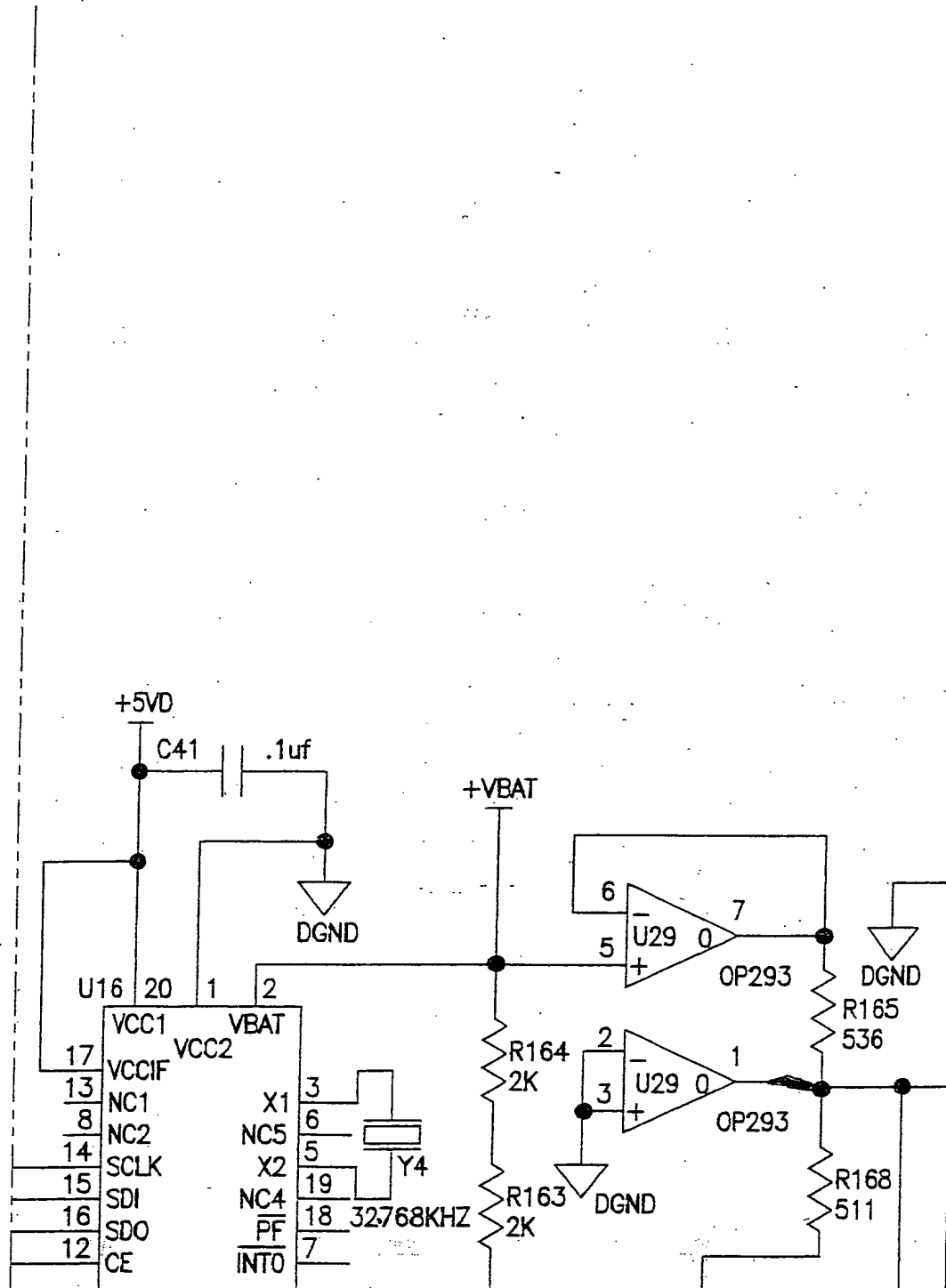
FIG. 62R





AMENDED SHEET SHOWING CHANGES

FIG. 64M





AMENDED SHEET SHOWING CHANGES

FIG. 66J

